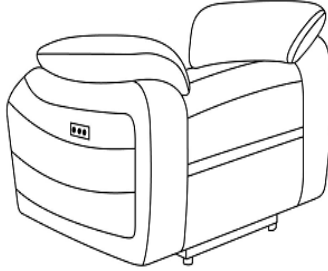
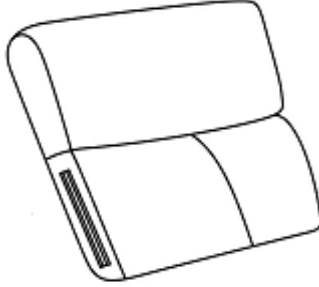


Assembly instruction

A*1	B*1	C*1	D*1
		 L	 R

